

March 1989 Revised March 2000

DM74LS266 Quad 2-Input Exclusive-NOR Gate with Open-Collector Outputs

General Description

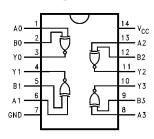
This device contains four independent gates each of which performs the logic exclusive-NOR function. Outputs are open collector.

Ordering Code:

Order Number	Package Number	Package Description	
DM74LS266M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow	
DM74LS266N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide	

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Truth Table

Inp	uts	Outputs	
Α	В	Y	
L	L	Н	
L	Н	L	
Н	L	L	
Н	Н	Н	

H = HIGH Voltage Level L = LOW Voltage Level